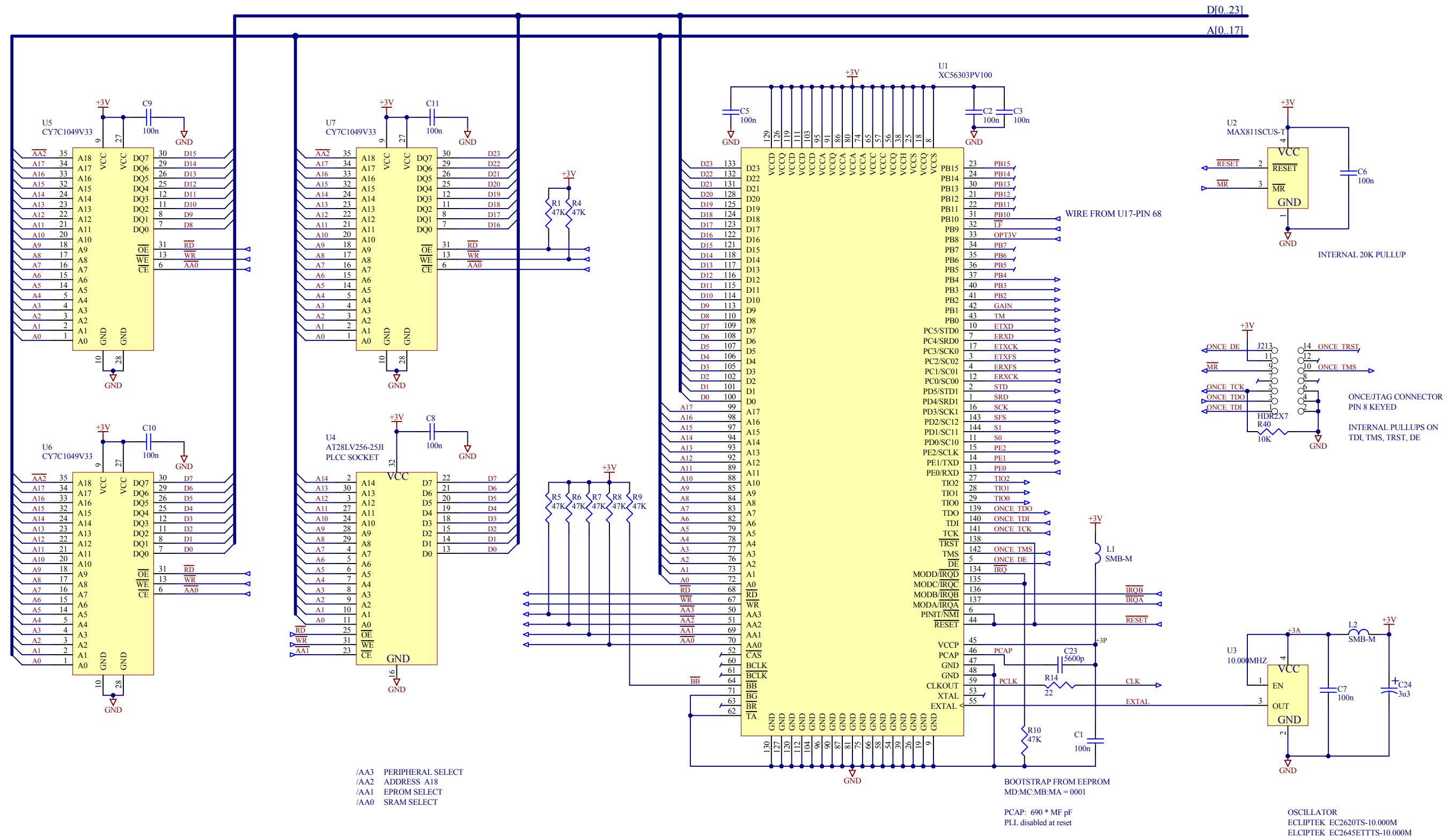


SRAM + BOOTPROM

DIGITAL SIGNAL PROCESSOR



Title <i>BASE DSP TIMING PCB</i>		<i>OCIW</i> <i>813 Santa Barbara Street</i> <i>Pasadena, CA 91101</i> <i>www.ociw.edu</i>
Revision: 3.0b	Date: 9/30/2005	
Sheet 1 of 3	File: C:\base\dsp_v3\dspA.SchDoc	

