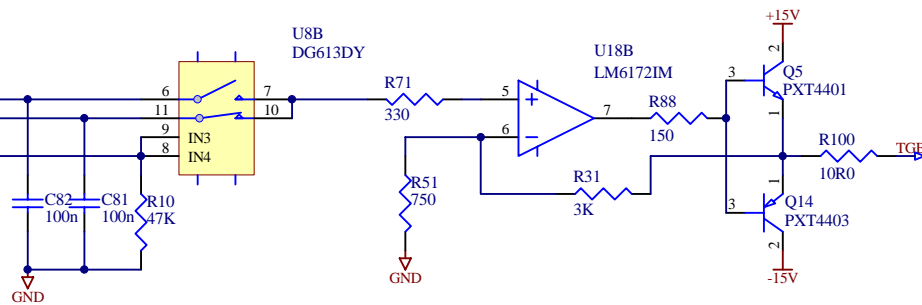
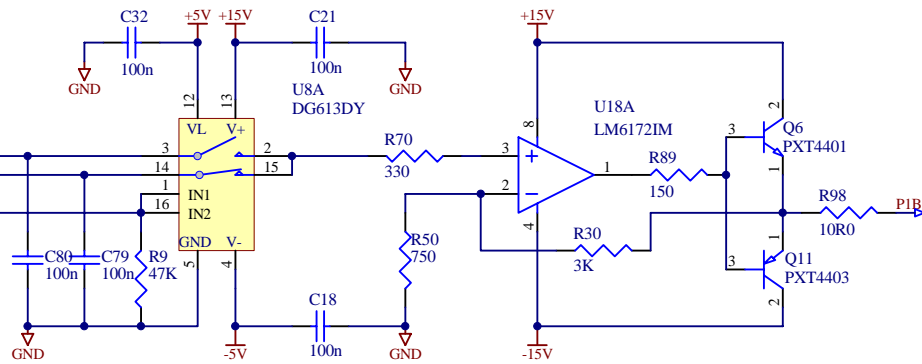
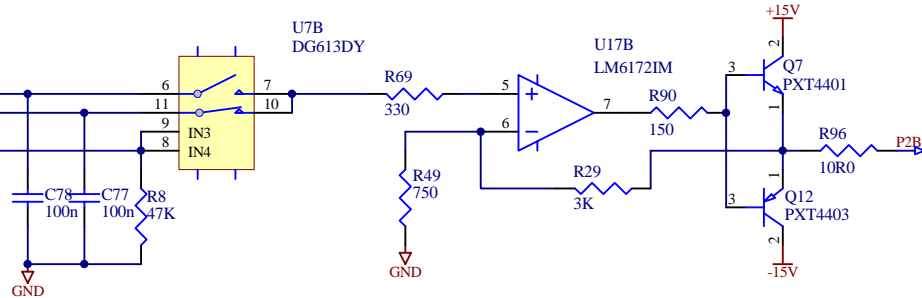
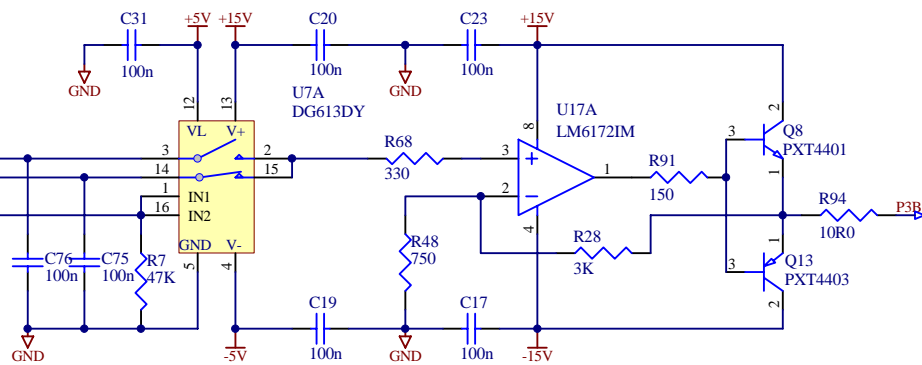
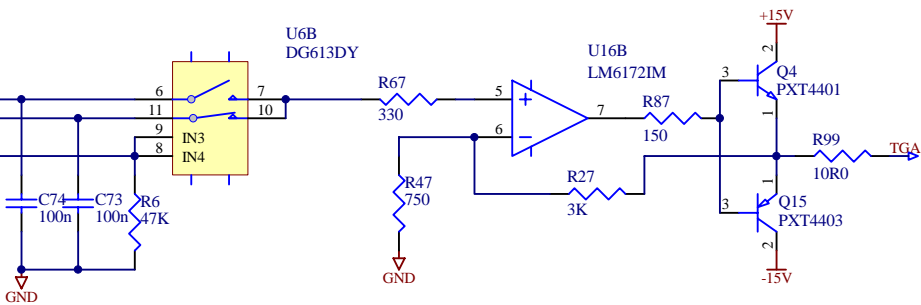
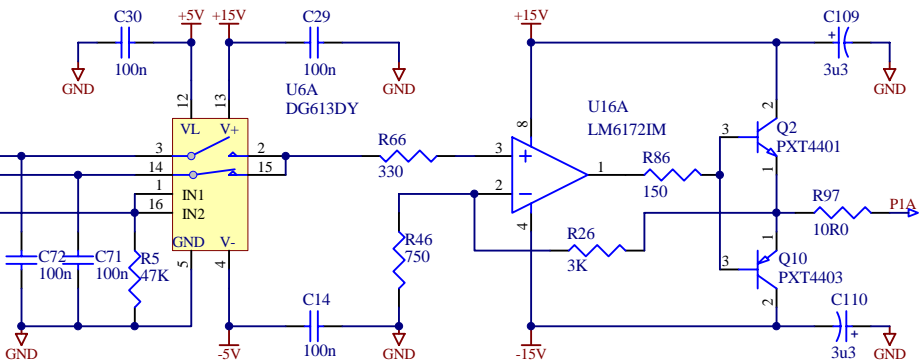
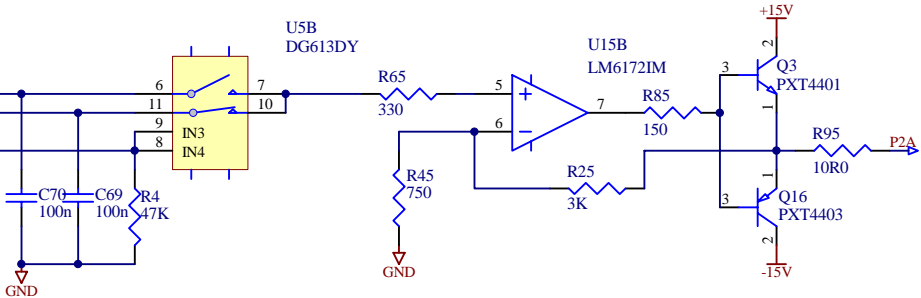
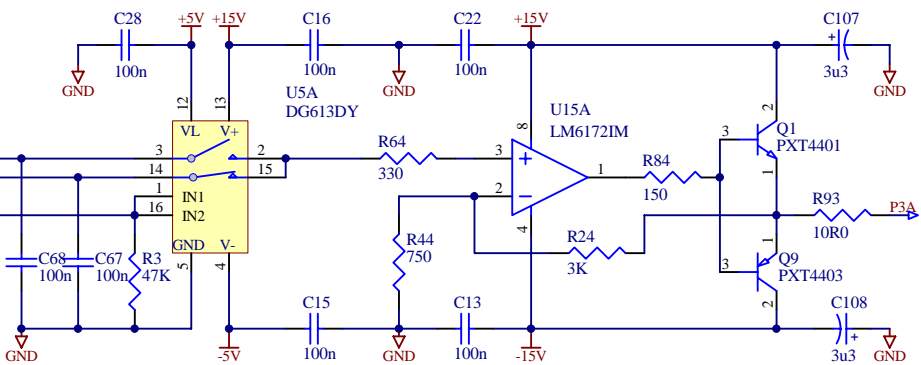


SEQ[19..0]

V[31..0]



GAIN = 5 OUTPUT = 5 mV/DN

Title		OCIW 813 Santa Barbara Street Pasadena, CA 91101 www.ociw.edu
<b>BASE CLOCK DRIVER PCB</b>		
Revision: 3.0a	Date: 1/11/2011	
Sheet 2 of 5	File: G:\pcb\base\clkdrv_v3\clkdrvB.SchDoc	