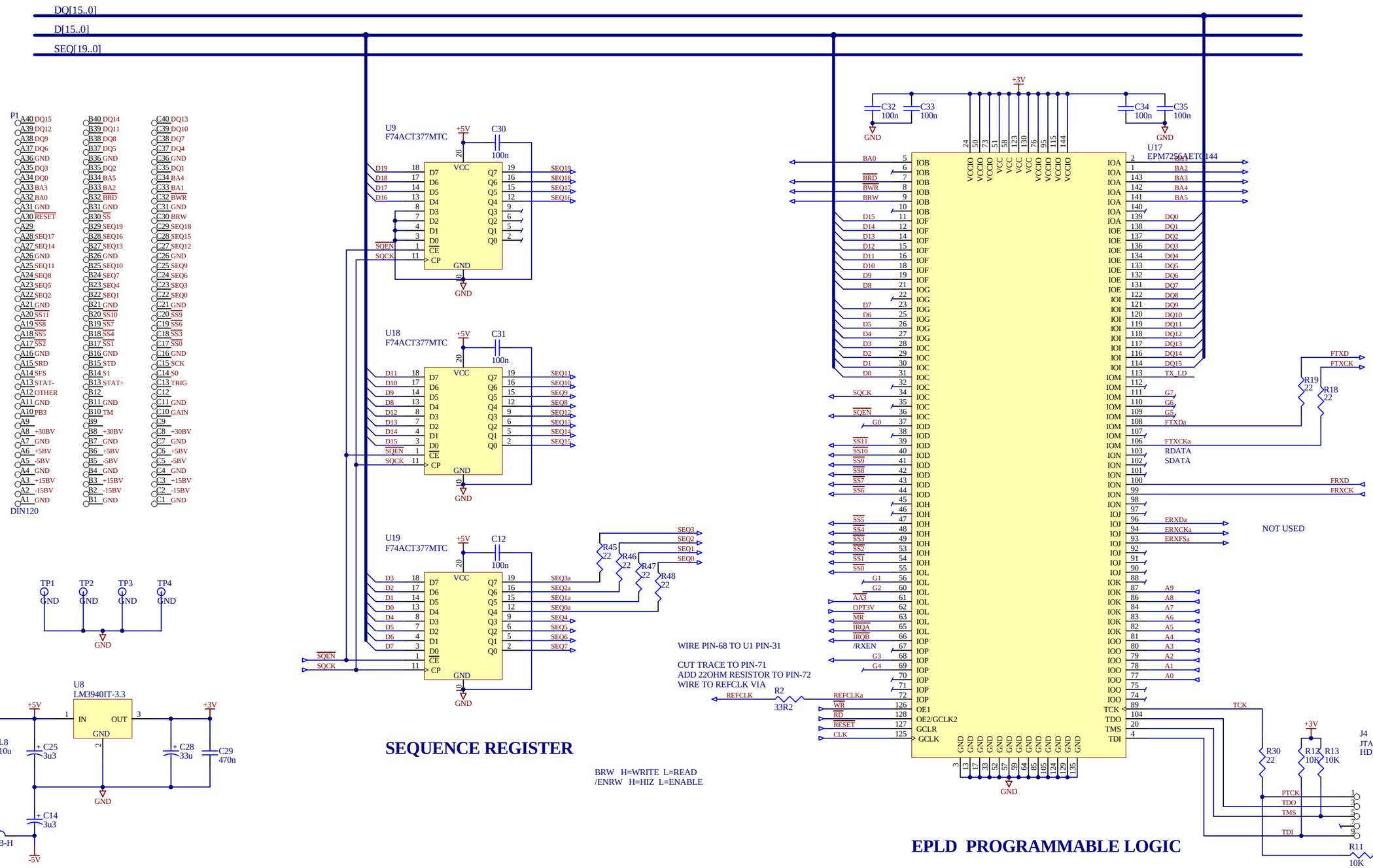

Title	BASE DSP TIMING PCB
Revision: 3.1	Date: 1/11/2011
Sheet 2 of 3	File: G:\pcb\base\dsp_v3.1\dspB.SchDoc

OCIW
813 Santa Barbara Street
Pasadena, CA 91101
www.ociw.edu