D[0..23]
SEQ[0..19]
V[0..15]

U1
MAX547ACMH

U2
MAX547ACMH

SWITCHES SHOWN FOR LOGIC 0

GAIN = 5 OUTPUT = 5 mV/DN

Title	GUIDER CAMERA CLOCK DRIVER PCB
Revision:	2
Date:	7/6/2005
Sheet	1 of 3
File:	C:\gcam\clkdrv_v2\clkdrvA.SchDoc

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