

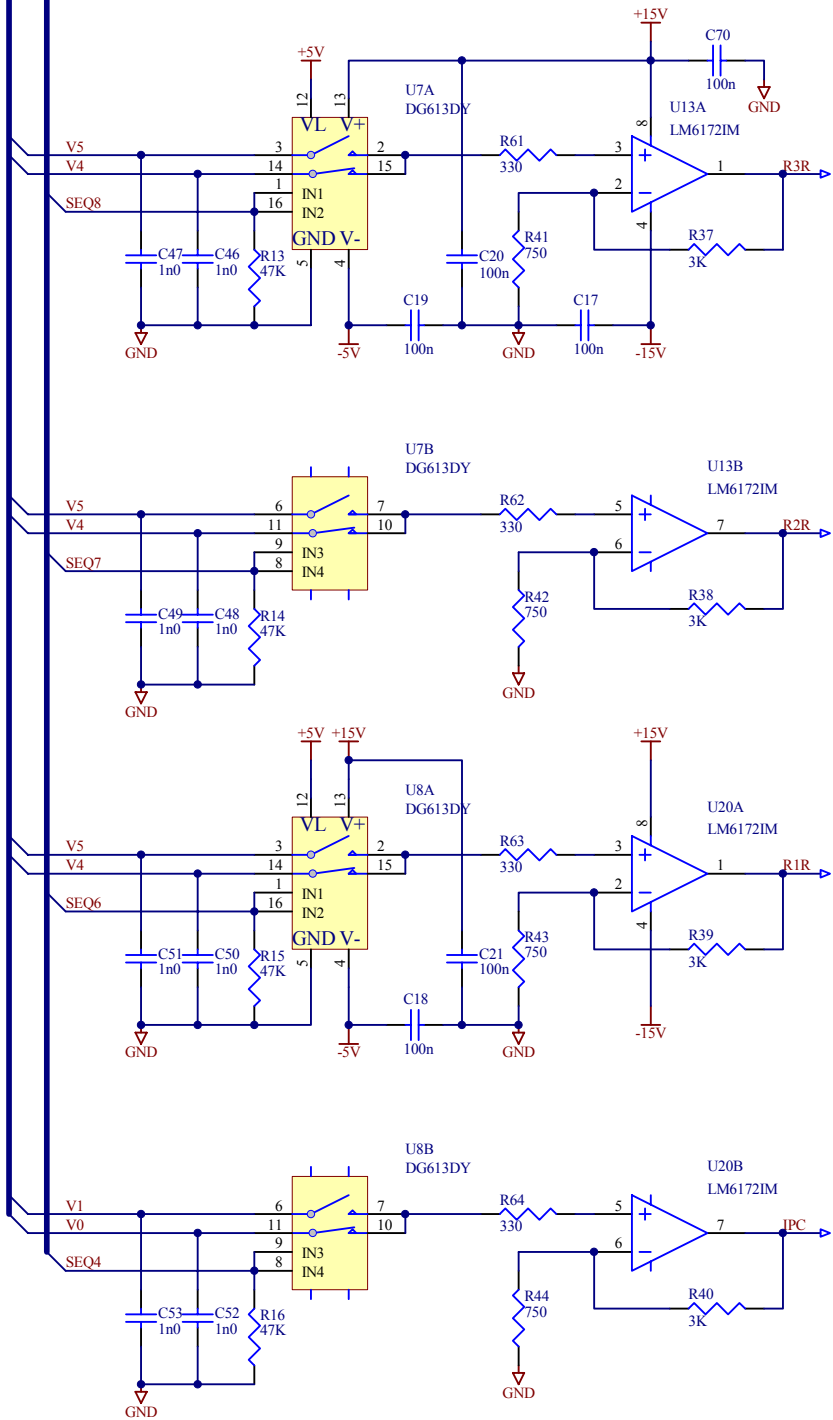
SWITCHES SHOWN FOR LOGIC 0

GAIN = 5    OUTPUT = 5 mV/DN

Title		OCIW	
GUIDER CAMERA CLOCK DRIVER PCB		813 Santa Barbara Street Pasadena, CA 91101 www.ociw.edu	
Revision:	2	Date:	7/6/2005
Sheet	1 of 3	File:	C:\gcam\clkdrv_v2\clkdrvA.SchDoc

SEQ0..19]

V[0..15]



J1A  
1 D15  
3 D13  
5 D11  
7 D9  
9 GND  
11 D7  
13 D5  
15 D3  
17 D1  
19 GND  
21 RESET  
23 A1  
25 WR  
27 CS5  
29 GND  
31 CS3  
33 CS1  
35 SEQ19  
37 SEQ17  
39 GND  
41 SEQ15  
43 SEQ13  
45 SEQ11  
47 SEQ9  
49 GND  
51 SEQ7  
53 SEQ5  
55 SEQ3  
57 SEQ1  
59 GND  
61 IM3  
63 IM1  
65 ST2  
67 TG  
69 GND  
71 RG  
73 R3L  
75 R1L  
77 R2R  
79 GND  
81  
83  
85  
87  
89 GND  
91 +5BV  
93  
95 +15BV  
97 -15BV  
99 GND  
TFM-150-01-S-D-RA

J1B  
2 D14  
4 D12  
6 D10  
8 D8  
10 GND  
12 D6  
14 D4  
16 D2  
18 D0  
20 GND  
22 A2  
24 A0  
26 RD  
28 CS4  
30 GND  
32 CS2  
34 CS0  
36 SEQ18  
38 SEQ16  
40 GND  
42 SEQ14  
44 SEQ12  
46 SEQ10  
48 SEQ8  
50 GND  
52 SEQ6  
54 SEQ4  
56 SEQ2  
58 SEQ0  
60 GND  
62 IM2  
64 ST3  
66 ST1  
68 DG  
70 GND  
72 IPC  
74 R2L  
76 R3R  
78 R1R  
80 GND  
82  
84  
86  
88  
90 GND  
92 +5BV  
94 +30BV  
96 +15BV  
98 -15BV  
100 GND  
TFM-150-01-S-D-RA

