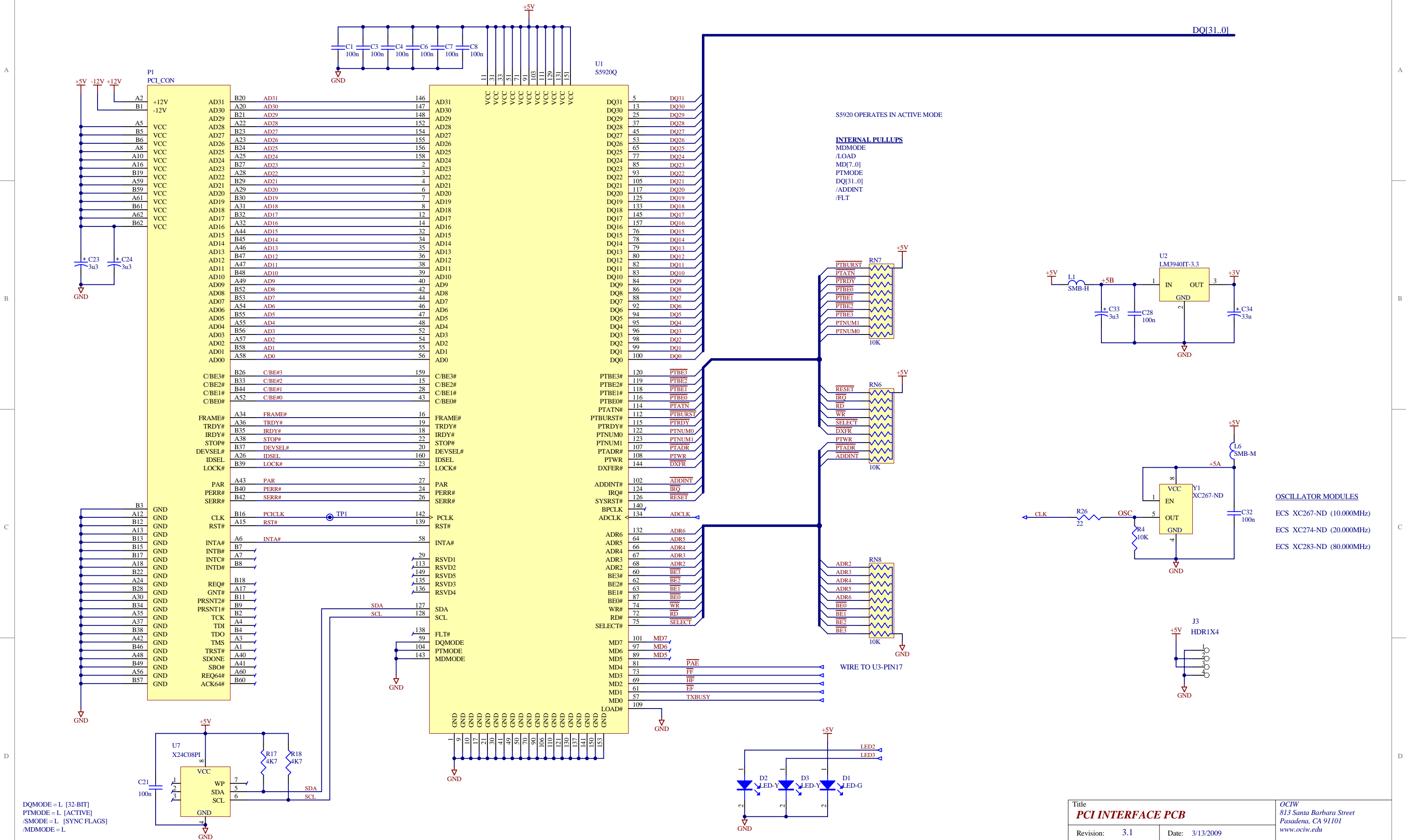
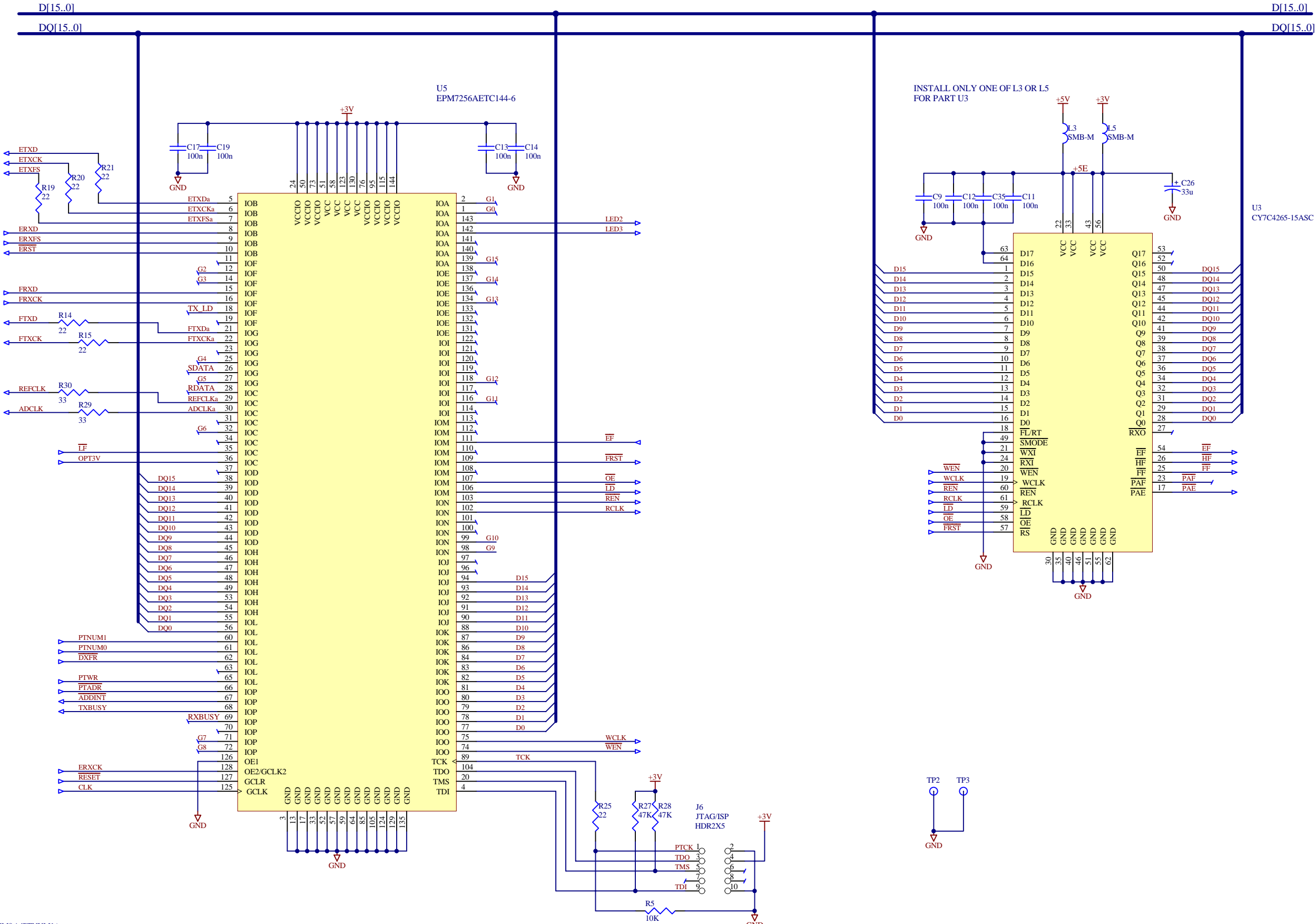


PCI TO ADD-ON INTERFACE



EPLD PROGRAMMABLE LOGIC

FIFO



Title		OCIW	
Revision: 3.1		813 Santa Barbara Street	
Date: 3/13/2009		Pasadena, CA 91101	
File: C:\pcb\pci\pci_v3.1\pciB.SchDoc		www.ociw.edu	
Sheet 2 of 3			

